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RE APPLICATION OF:

Makoto NAKAYAMA, et al.

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EXAMINER:

FOR:

GATEWAY APPARATUS, AND METHOD FOR PROCESSING SIGNALS IN

THE GATEWAY APPARATUS

LETTER

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Mail Stop DD Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Submitted herewith is a Korean Office Action for the Examiner's consideration. The reference cited therein has been previously filed on January 25, 2005. Also submitted herewith is a PTO-1449 listing the unedited computer-generated English translation of a previously submitted reference JP 2001-292251.

Respectfully Submitted,

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